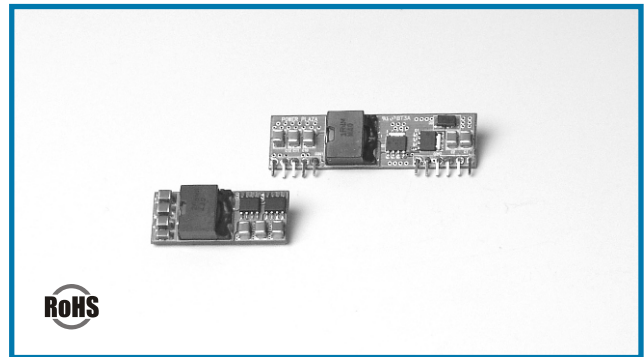


SNS15A

Non-isolated DC/DC POL converter

Total output current **15 A**
Input voltages **5V, 12V**
#of outputs **Single output**



SPECIAL FEATURES

- Delivers 15A of output Current
- High efficiency
- Low Output Ripple & Noise
- Small size and low profile 50.8mm x 13.97mm x 8.64mm (SIP package)
- Cost-efficient open frame design
- Single - in - line (SIP) & SMT package
- Remote ON/OFF
- Remote sense
- Trimmable output voltage via external resistor
- Output Over current protection
- Over temperature protection
- Wide Operating temperature range -40 ~85

ELECTRICAL SPECIFICATIONS

SNS 15A - 5 SIP Packages

- Efficiency 94% @ 3.3V
- Input voltage range 3.0V - 5.5V
- Output voltage 1.0V - 3.3V
- Voltage tolerance $\pm 2.0\%$
- Line regulation $\pm 0.3\%$
- Load regulation $\pm 0.4\%$
- Switching frequency 300KHz
- *Ripple & noise 40mVp-p (@3.3V)
- MTBF (MIL-HDBK-217F) 8.0×10^5 hrs

SNS 15A - 12 SIP Packages

- Efficiency 94% @ 5.0V
- Input voltage range 10.0V - 14.0V
- Output voltage 1.0V - 5.0V
- Voltage tolerance $\pm 2.0\%$
- Line regulation $\pm 0.3\%$
- Load regulation $\pm 0.4\%$
- Switching frequency 300KHz
- *Ripple & noise 60mVp-p (@5.0V)
- MTBF (MIL-HDBK-217F) 7.6×10^5 hrsF

* Ripple & noise is tested / specified over a 20MHz bandwidth and may be reduced with external filtering.

Ordering Information

Input	Output	Maximum Power	Ripple & Noise max	Efficiency Typ.	Model Number
3.0-5.5V	1.0V@15A	15.0W	40mVp-p	84%	SNS15A-5-1R0
3.0-5.5V	1.5V@15A	22.5W	40mVp-p	88%	SNS15A-5-1R5
3.0-5.5V	1.8V@15A	27.0W	40mVp-p	89%	SNS15A-5-1R8
3.0-5.5V	2.0V@15A	30.0W	40mVp-p	91%	SNS15A-5-2R0
3.0-5.5V	2.5V@15A	37.5W	40mVp-p	92%	SNS15A-5-2R5
4.5-5.5V	3.3V@15A	49.5W	40mVp-p	94%	SNS15A-5-3R3
10-14V	1.0V@15A	15.0W	60mVp-p	80%	SNS15A-12-1R0
10-14V	1.5V@15A	22.5W	60mVp-p	85%	SNS15A-12-1R5
10-14V	1.8V@15A	27.0W	60mVp-p	87%	SNS15A-12-1R8
10-14V	2.5V@15A	37.5W	60mVp-p	90%	SNS15A-12-2R5
10-14V	3.3V@15A	49.5W	60mVp-p	92%	SNS15A-12-3R3
10-14V	5.0V@15A	75.0W	60mVp-p	94%	SNS15A-12-5R0

1. Typical at $T_a = +25^\circ\text{C}$ under nominal line voltage and full load conditions, unless otherwise noted. All models are tested and specified with external input and output capacitors. ($C_{in}=100\mu\text{F} \times 2$ $C_{out}=10\mu\text{F}$), These capacitors are necessary to accommodate Our test equipment
2. Ripple & Noise is tested / specified over a 20MHz bandwidth and may be reduced with external filtering. See I/O Filtering
3. These device have no minimum-load requirement and will regulate Under no-load conditions. Regulation specification describe the output voltage deviation as the line voltage or load is varied from its minimum value to either extreme.

Pin assignment

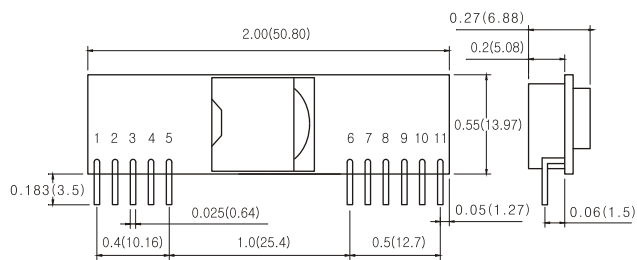
Single Output (SIP)

1. +Vout
2. +Vout
3. Sense
4. +Vout
5. COM
6. COM
7. +Vin
8. +Vin
9. No Pin
10. TRIM
11. ON/OFF

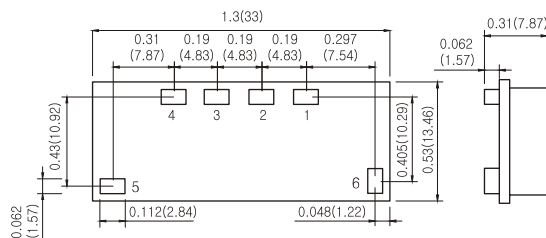
Single Output (SMT)

1. Sense
2. Trim
3. +Vout
4. COM
5. +Vin
6. ON/OFF

Dimensions



<SIP TYPE>



Bottom view

Side view

<SMT TYPE>

NOTES

1. All dimensions are in inches (mm)
2. Weight : 8.0g or less (SIP)
5.8g or less (SMT)